FORM 4

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UNITED STATES SECURITIES AND EXCHANGE COMMISSION

Washington, D.C. 20549

STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP

OMB APPROVAL

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may continue. See	e Instruction 1(b).	-	Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934 or Section 30(h) of the Investment Company Act of 1940		
1. Name and Addres <u>DREHOBL S</u>		on	2. Issuer Name and Ticker or Trading Symbol <u>MICROCHIP TECHNOLOGY INC</u> [MCHP]	5. Relationship of Reporting Per (Check all applicable) Director	10% Owner
(Last) C/O MICROCHI 2355 WEST CH		(Middle) GY INCORPORATED JEVARD	3. Date of Earliest Transaction (Month/Day/Year) 10/01/2021	X Officer (give title below) Senior VP, MCU	Other (specify below) 18/MCU16 BU
(Street) CHANDLER	AZ	85224-6199	4. If Amendment, Date of Original Filed (Month/Day/Year)	6. Individual or Joint/Group Film X Form filed by One Rej Form filed by More that	
(City)	(State)	(Zip)			

Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned

1. Title of Security (Instr. 3)	Date		Code (Instr.		Disposed Of (D) (Instr. 3, 4 and 5)			5. Amount of Securities Beneficially Owned Following Reported	6. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	7. Nature of Indirect Beneficial Ownership (Instr. 4)
			Code	v	Amount	(A) or (D)	Price	 Transaction(s) (Instr. 3 and 4) 		(1150. 4)
Common Stock								19,757	D	

Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned (e.g., puts, calls, warrants, options, convertible securities)

1. Title of Derivative Security (Instr. 3)	2. Conversion or Exercise Price of Derivative Security	3. Transaction Date (Month/Day/Year)	Code (li	5. Number of Derivative Securities Acquired (A) or Disposed of (D) (Instr. 3, 4 and 5)		6. Date Exercisable and Expiration Date (Month/Day/Year)		7. Title and Amount of Securities Underlying Derivative Security (Instr. 3 and 4)		8. Price of Derivative Security (Instr. 5)	9. Number of derivative Securities Beneficially Owned Following Reported Transaction(s)	10. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	11. Nature of Indirect Beneficial Ownership (Instr. 4)	
			Code	v	(A)	(D)	Date Exercisable	Expiration Date	Title	Amount or Number of Shares		(Instr. 4)		
Restricted Stock Units	(1)	10/01/2021	Α		973		(2)	(2)	Common Stock	973	\$ <mark>0</mark>	973	D	
Performance Stock Units	(3)	10/01/2021	Α		974		(4)	(4)	Common Stock	974	\$ <mark>0</mark>	974	D	
Restricted Stock Units	(1)	10/01/2021	Α		7,784		(5)	(5)	Common Stock	7,784	\$ <mark>0</mark>	7,784	D	
Performance Stock Units	(3)	10/01/2021	Α		3,896		(6)	(6)	Common Stock	3,896	\$ 0	3,896	D	

Explanation of Responses:

1. Each restricted stock unit represents a contingent right to receive one share of Microchip Technology Incorporated common stock

2. The restricted stock units will vest in full on November 15, 2025 as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

3. Each performance stock unit represents a contingent right to receive one share of Microchip Technology Incorporated common stock.

4. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin of 20 granters ending September 30, 2024. The target number of PSU shares that may be earned in steported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 40.0% over the 12 quarter seaturement period. The actual number of shares that may be earned and be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on November 15, 2025 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

5. The restricted stock units vest in four quarterly installments of 974 shares beginning November 15, 2023, and eight quarterly installments of 486 shares beginning on November 15, 2024, as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

6. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending September 30, 2024. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip's non-GAAP operating margin of 40.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin or et depending on Microchip's non-GAAP operating margin or SUSUs will vest ratably over eight quarters beginning on November 15, 2024 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

Remarks:

Deborah L. Wussler, as Attorney-10/05/2021

in-Fact

** Signature of Reporting Person Date

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

* If the form is filed by more than one reporting person, see Instruction 4 (b)(v).

** Intentional misstatements or omissions of facts constitute Federal Criminal Violations See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, see Instruction 6 for procedure.

Persons who respond to the collection of information contained in this form are not required to respond unless the form displays a currently valid OMB Number.