# FORM 4

## UNITED STATES SECURITIES AND EXCHANGE COMMISSION

Washington, D.C. 20549

## STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP

OMB APPROVAL

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Check this box if no longer subject to Section 16. Form 4 or Form 5 obligations may continue. See Instruction 1(b).

Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934 or Section 30(h) of the Investment Company Act of 1940

1. Name and Address			2. Issuer Name and Ticker or Trading Symbol MICROCHIP TECHNOLOGY INC [ MCHP ]			on(s) to Issuer
MOORTHY G	ANESH		[ [	X	Director	10% Owner
(Last)	(First)	(Middle) 3. Date of Earliest Transaction (Month/Day/Year) 0 0 10% Owner   10/01/2021 0 0 0 0				
C/O MICROCHIP	P TECHNOLOGY I	NCORPORATED			President &	ceo
2355 WEST CHA	NDLER BOULEVA	RD				
(Street) CHANDLER	AZ	85224-6199	4. If Amendment, Date of Original Filed (Month/Day/Year)		Form filed by One Repo	orting Person
(City)	(State)	(Zip)				gg

### Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned

1. Title of Security (Instr. 3)	2. Transaction Date (Month/Day/Year)	2A. Deemed Execution Date, if any (Month/Day/Year)	Transaction Code (Instr.		4. Securities Acquired (A) or Disposed Of (D) (Instr. 3, 4 and 5)			5. Amount of Securities Beneficially Owned Following Reported Transaction(s)	6. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	7. Nature of Indirect Beneficial Ownership
			Code	v	Amount	(A) or (D)	Price	(Instr. 3 and 4)		(Instr. 4)
Common Stock								339,790	I	Shares held Indirectly, by Trust.

Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned (e.g., puts, calls, warrants, options, convertible securities)

1. Title of Derivative Security (Instr. 3)	2. Conversion or Exercise Price of Derivative Security	3. Transaction Date (Month/Day/Year)	3A. Deemed Execution Date, if any (Month/Day/Year)	Code (I	5. Number of Derivative Securities Acquired (A) or Disposed of (D) (Instr. 3, 4 and 5)		6. Date Exercisable and Expiration Date (Month/Day/Year)		7. Title and Amount of Securities Underlying Derivative Security (Instr. 3 and 4)		8. Price of Derivative Security (Instr. 5)	9. Number of derivative Securities Beneficially Owned Following Reported	10. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	11. Nature of Indirect Beneficial Ownership (Instr. 4)	
				Code	v	(A)	(D)	Date Exercisable	Expiration Date	Title	Amount or Number of Shares		Transaction(s) (Instr. 4)		
Restricted Stock Units	(1)	10/01/2021		Α		3,202		(2)	(2)	Common Stock	3,202	\$0	3,202	D	
Performance Stock Units	(3)	10/01/2021		Α		3,202		(4)	(4)	Common Stock	3,202	\$0	3,202	D	
Restricted Stock Units	(1)	10/01/2021		А		25,614		(5)	(5)	Common Stock	25,614	\$ <u>0</u>	25,614	D	
Performance Stock Units	(3)	10/01/2021		A		12,808		(6)	(6)	Common Stock	12,808	\$0	12,808	D	

#### Explanation of Responses:

1. Each restricted stock unit represents a contingent right to receive one share of Microchip Technology Incorporated common stock.

2. The restricted stock units will vest in full on November 15, 2025 as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

3. Each performance stock unit represents a contingent right to receive one share of Microchip Technology Incorporated common stock.

4. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin of 12 quarters ending September 30, 2024. The target number of PSU shares that may be earned as proteid in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 40.0% over the 12 quarter seasurement period. The actual number of shares that may be earned and be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on November 15, 2025 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

5. The restricted stock units vest in two quarterly installments of 3,201 shares beginning November 15, 2023, two quarterly installment of 3,202 shares on May 15, 2024, and eight quarterly installments of 1,601 shares beginning on November 15, 2024, as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

6. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending September 30, 2024. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip's non-GAAP operating margin of 40.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin or ending on Microchip's non-GAAP operating margin or be than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest ratably over eight quarters beginning on November 15, 2024 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

#### Remarks:

Deborah L. Wussler, as Attorney-10/05/2021

in-Fact \*\* Signature of Reporting Person

Date

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

 $^{\star}$  If the form is filed by more than one reporting person, see Instruction 4 (b)(v).

\*\* Intentional misstatements or omissions of facts constitute Federal Criminal Violations See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, see Instruction 6 for procedure.

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