FORM 4

UNITED STATES SECURITIES AND EXCHANGE COMMISSION

Washington, D.C. 20549

STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP

OMB APPROVAL

OMB Number: 3235-0287 Estimated average burden hours per response 0.5

	no longer subject to	_		hours	per response:	
	n 4 or Form 5 obligatio ee Instruction 1(b).	ns	Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934 or Section 30(h) of the Investment Company Act of 1940			
SIMONCIC (Last) C/O MICROCH	ess of Reporting Per RICHARD J (First) HIP TECHNOLO HANDLER BOU	(Middle) GY INCORPORATED	2. Issuer Name and Ticker or Trading Symbol <u>MICROCHIP TECHNOLOGY INC</u> [MCHP] 3. Date of Earliest Transaction (Month/Day/Year) 10/01/2021		tionship of Reporting P all applicable) Director Officer (give title below) Senior VP, An	erson(s) to Issuer 10% Owner Other (specify below) alog/Intrfce BU
(Street) CHANDLER (City)	AZ (State)	85224-6199 (Zip)	4. If Amendment, Date of Original Filed (Month/Day/Year)	6. Indivi X	Form filed by One R	ing (Check Applicable Line) teporting Person than One Reporting Person

Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned

1. Title of Security (Instr. 3)	2. Transaction Date (Month/Day/Year)	Execution Date, Transaction Disposed Of (D) (Instr. 3, 4 and 5			Securities	6. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	7. Nature of Indirect Beneficial Ownership			
			Code	v	Amount	(A) or (D)	Price	(Instr. 3 and 4)		(Instr. 4)
Common Stock								53,355	Ι	Shares held Indirectly, by Trust.

Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned (e.g., puts, calls, warrants, options, convertible securities)

1. Title of Derivative Security (Instr. 3)	2. Conversion or Exercise Price of Derivative Security	3. Transaction Date (Month/Day/Year)	3A. Deemed Execution Date, if any (Month/Day/Year)		Transaction Derivative Code (Instr. Securities		6. Date Exercisable and Expiration Date (Month/Day/Year)		7. Title and Amount of Securities Underlying Derivative Security (Instr. 3 and 4)		8. Price of Derivative Security (Instr. 5)	Reported	10. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	11. Nature of Indirect Beneficial Ownership (Instr. 4)	
				Code	v	(A)	(D)	Date Exercisable	Expiration Date	Title	Amount or Number of Shares		Transaction(s) (Instr. 4)		
Restricted Stock Units	(1)	10/01/2021		Α		702		(2)	(2)	Common Stock	702	\$ <mark>0</mark>	702	D	
Performance Stock Units	(3)	10/01/2021		Α		703		(4)	(4)	Common Stock	703	\$ <mark>0</mark>	703	D	
Restricted Stock Units	(1)	10/01/2021		Α		5,621		(5)	(5)	Common Stock	5,621	\$ <mark>0</mark>	5,621	D	
Performance Stock Units	(3)	10/01/2021		A		2,808		(6)	(6)	Common Stock	2,808	\$ 0	2,808	D	

Explanation of Responses:

1. Each restricted stock unit represents a contingent right to receive one share of Microchip Technology Incorporated common stock.

2. The restricted stock units will vest in full on November 15, 2025 as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

3. Each performance stock unit represents a contingent right to receive one share of Microchip Technology Incorporated common stock.

4. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's normal relation back of the formation of

5. The restricted stock units vest in three quarterly installments of 703 shares beginning November 15, 2023, one quarterly installment of 351 shares on November 15, 2024, one quarterly installment of 352 shares on February 15, 2025 and six quarterly installments of 351 shares beginning on May 15, 2025 as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

6. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending September 30, 2024. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 40.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest ratably over eight quarters beginning on November 15, 2024 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

Remarks:

Deborah L. Wussler, as Attorney-10/05/2021

** Signature of Reporting Person

in-Fact

Date

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

* If the form is filed by more than one reporting person, see Instruction 4 (b)(v).

** Intentional misstatements or omissions of facts constitute Federal Criminal Violations See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, see Instruction 6 for procedure.

Persons who respond to the collection of information contained in this form are not required to respond unless the form displays a currently valid OMB Number.