FORM 4

## **UNITED STATES SECURITIES AND EXCHANGE COMMISSION**

Washington, D.C. 20549

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### STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP

Check this box if no longer subject to Section 16. Form 4 or Form 5 obligations may continue. See Instruction 1(b).

Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934 or Section 30(h) of the Investment Company Act of 1940

			2. Issuer Name and Ticker or Trading Symbol MICROCHIP TECHNOLOGY INC [ MCHP ]	5. Relationship of Reporting Person(s) to Issuer (Check all applicable)					
			MICHOCIM IECINOEGGI III	X	Director	10% Owner			
(Last) (First) (Middle)		(Middle)		X	Officer (give title below)	Other (specify below)			
C/O MICROCHIP TECHNOLOGY INCORPORATED 2355 WEST CHANDLER BOULEVARD			3. Date of Earliest Transaction (Month/Day/Year) 01/03/2023		Executive Chair				
(Street) CHANDLER AZ 85224-6199		85224-6199	4. If Amendment, Date of Original Filed (Month/Day/Year)	6. Indivi	Form filed by One Re	ng (Check Applicable Line) eporting Person nan One Reporting Person			
(City)	(State)	(Zip)			,	. 3			

### Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned

1. Title of Security (Instr. 3)	2. Transaction Date (Month/Day/Year)	2A. Deemed Execution Date, if any (Month/Day/Year)	Transaction Code (Instr.		4. Securities Acquired (A) or Disposed Of (D) (Instr. 3, 4 and 5)			5. Amount of Securities Beneficially Owned Following Reported Transaction(s)	6. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	7. Nature of Indirect Beneficial Ownership	
			Code	v	Amount	(A) or (D)	Price	(Instr. 3 and 4)		(Instr. 4)	
Common Stock								10,037,836	I	Shares held Indirectly, by Trust and by Family Limited Partnership.	

# Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned (e.g., puts, calls, warrants, options, convertible securities)

1. Title of Derivative Security (Instr. 3)	2. Conversion or Exercise Price of Derivative Security	3. Transaction Date (Month/Day/Year)	3A. Deemed Execution Date, if any (Month/Day/Year)	4. Transac Code (li 8)				Expiration Date (Month/Day/Year)		7. Title and Amount of Securities Underlying Derivative Security (Instr. 3 and 4)		8. Price of Derivative Security (Instr. 5)	9. Number of derivative Securities Beneficially Owned Following Reported	10. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	11. Nature of Indirect Beneficial Ownership (Instr. 4)
				Code	v	(A)	(D)	Date Exercisable	Expiration Date	Title	Amount or Number of Shares		Transaction(s) (Instr. 4)		
Restricted Stock Units	(2)	01/03/2023		Α		8,558		(3)	(3)	Common Stock	8,558	\$0	8,558	D	
Restricted Stock Units	(2)	01/03/2023		A		1,711		(4)	(4)	Common Stock	1,711	\$0	1,711	D	
Restricted Stock Units	(2)	01/03/2023		A		1,711		(5)	(5)	Common Stock	1,711	\$0	1,711	D	
Performance Stock Units	(6)	01/03/2023		A		8,558		(7)	(7)	Common Stock	8,558	\$0	8,558	D	
Performance Stock Units	(6)	01/03/2023		A		1,712		(8)	(8)	Common Stock	1,712	\$0	1,712	D	
Performance Stock Units	(6)	01/03/2023		A		1,712		(9)	(9)	Common Stock	1,712	\$0	1,712	D	

#### **Explanation of Responses:**

- 1. Of the 10,037,836 shares held, 4,131,964 shares were held by The Sanghi Trust; and 5,905,872 shares were held by The Sanghi Family Limited Partnership.
- 2. Each restricted stock unit represents a contingent right to receive one share of Microchip Technology Incorporated common stock.
- 3. The restricted stock units will vest in full on February 15, 2027 as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
- 4. The restricted stock units will vest in full on February 17, 2025 as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
- 5. The restricted stock units will vest in full on February 16, 2026 as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
- $6.\ Each\ performance\ stock\ unit\ represents\ a\ contingent\ right\ to\ receive\ one\ share\ of\ Microchip\ Technology\ Incorporated\ common\ stock.$
- 7. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending December 31, 2025. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip's non-GAAP operating margin of 40.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on February 15, 2027 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
- 8. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 8 quarters ending December 31, 2024. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip's non-GAAP operating margin of 40.0% over the 8 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on February 17, 2025 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
- 9. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending December 31, 2025. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 40.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on February 16, 2026 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

## Remarks:

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

- $^{\star}$  If the form is filed by more than one reporting person, see Instruction 4 (b)(v).
- \*\* Intentional misstatements or omissions of facts constitute Federal Criminal Violations See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, see Instruction 6 for procedure.

Persons who respond to the collection of information contained in this form are not required to respond unless the form displays a currently valid OMB Number.