FORM 4

UNITED STATES SECURITIES AND EXCHANGE COMMISSION

Washington, D.C. 20549

OMB APPE

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STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP

Check this box if no longer subject to Section 16. Form 4 or Form 5 obligations may continue. See Instruction 1(b).

Check this box to indicate that a ransaction was made pursuant to a contract, instruction or written plan for the purchase or sale of equity securities of the issuer that is intended to satisfy the affirmative defense conditions of Rule 10b5-1(c). See Instruction 10. Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934 or Section 30(h) of the Investment Company Act of 1940

1. Name and Address of Reporting Person* SIMONCIC RICHARD J			2. Issuer Name and Ticker or Trading Symbol MICROCHIP TECHNOLOGY INC [MCHP]	5. Relationship of Reporting Person(s) to Issuer (Check all applicable) Director 10% Owner X Officer (give title Other (specify				
	Last) (First) (Middle) C/O MICROCHIP TECHNOLOGY INCORPORATED 2355 WEST CHANDLER BOULEVARD		3. Date of Earliest Transaction (Month/Day/Year) 04/03/2024	below) below) Chief Operating Officer				
Street) CHANDLER AZ 85224-6199 (City) (State) (Zip)			4. If Amendment, Date of Original Filed (Month/Day/Year)	Individual or Joint/Group Filing (Check Applicable Line) X Form filed by One Reporting Person Form filed by More than One Reporting Person				

Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned

1. Title of Security (Instr. 3)	2. Transaction Date (Month/Day/Year)	2A. Deemed Execution Date, if any (Month/Day/Year)	3. Transaction Code (Instr. 8)		4. Securities Acquired (A) or Disposed Of (D) (Instr. 3, 4 and 5)			5. Amount of Securities Beneficially Owned Following Reported Transaction(s)	6. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	7. Nature of Indirect Beneficial Ownership
			Code	v	Amount	(A) or (D)	Price	(Instr. 3 and 4)		(Instr. 4)
Common Stock								131,735	I	Shares held Indirectly, by Trust.

Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned (e.g., puts, calls, warrants, options, convertible securities)

1. Title of Derivative Security (Instr. 3)	2. Conversion or Exercise Price of Derivative Security	3. Transaction Date (Month/Day/Year)	3A. Deemed Execution Date, if any (Month/Day/Year)	4. Transac Code (li 8)				6. Date Exercisable and Expiration Date (Month/Day/Year)		7. Title and Amount of Securities Underlying Derivative Security (Instr. 3 and 4)		8. Price of Derivative Security (Instr. 5)	9. Number of derivative Securities Beneficially Owned Following Reported	10. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	11. Nature of Indirect Beneficial Ownership (Instr. 4)
				Code	v	(A)	(D)	Date Exercisable	Expiration Date	Title	Amount or Number of Shares		Transaction(s) (Instr. 4)		
Performance Stock Units	(1)	04/03/2024		Α		3,483		(2)	(2)	Common Stock	3,483	\$0	3,483	D	
Restricted Stock Units	(3)	04/03/2024		Α		3,482		(4)	(4)	Common Stock	3,482	\$0	3,482	D	
Restricted Stock Units	(3)	04/03/2024		A		99		(5)	(5)	Common Stock	99	\$0	99	D	
Performance Stock Units	(1)	04/03/2024		A		934		(6)	(6)	Common Stock	934	\$0	934	D	
Restricted Stock Units	(3)	04/03/2024		A		933		(7)	(7)	Common Stock	933	\$0	933	D	

Explanation of Responses:

- 1. Each performance stock unit represents a contingent right to receive one share of Microchip Technology Incorporated common stock.
- 2. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending March 31, 2027. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 42.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on May 15, 2028 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
- 3. Each restricted stock unit represents a contingent right to receive one share of Microchip Technology Incorporated common stock
- 4. The restricted stock units will vest in full on May 15, 2028 as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
- 5. The restricted stock units will vest in full on February 16, 2026 as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
- 6. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending March 31, 2027. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 42.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on May 17, 2027 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
- 7. The restricted stock units will vest in full on May 17, 2027 as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

Remarks:

<u>Deborah L. Wussler, as Attorney-in-Fact</u>

04/05/2024

** Signature of Reporting Person

Date

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

- * If the form is filed by more than one reporting person, see Instruction 4 (b)(v).
- ** Intentional misstatements or omissions of facts constitute Federal Criminal Violations See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, see Instruction 6 for procedure.

Persons who respond to the collection of information contained in this form are not required to respond unless the form displays a currently valid OMB Number.