

INITIAL STATEMENT OF BENEFICIAL OWNERSHIP OF SECURITIES

Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934
or Section 30(h) of the Investment Company Act of 1940

OMB Number:	3235-0104
Estimated average burden hours per response:	0.5

1. Name and Address of Reporting Person* <u>Krawczyk Joseph R II</u> (Last) (First) (Middle) <u>C/O MICROCHIP TECHNOLOGY</u> <u>INCORPORATED</u> <u>2355 W CHANDLER BLVD</u> (Street) <u>CHANDLER</u> <u>AZ</u> <u>85224-6199</u> (City) (State) (Zip)	2. Date of Event Requiring Statement (Month/Day/Year) <u>08/20/2024</u>	3. Issuer Name and Ticker or Trading Symbol <u>MICROCHIP TECHNOLOGY INC</u> [<u>MCHP</u>]	4. Relationship of Reporting Person(s) to Issuer (Check all applicable) Director 10% Owner <input checked="" type="checkbox"/> Officer (give title below) Other (specify below) <u>SR. VP, WW CLIENT ENGAGEMENT</u>	5. If Amendment, Date of Original Filed (Month/Day/Year) 6. Individual or Joint/Group Filing (Check Applicable Line) <input checked="" type="checkbox"/> Form filed by One Reporting Person Form filed by More than One Reporting Person
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Table I - Non-Derivative Securities Beneficially Owned

1. Title of Security (Instr. 4)	2. Amount of Securities Beneficially Owned (Instr. 4)	3. Ownership Form: Direct (D) or Indirect (I) (Instr. 5)	4. Nature of Indirect Beneficial Ownership (Instr. 5)
Common Stock	12,967	D	

Table II - Derivative Securities Beneficially Owned
(e.g., puts, calls, warrants, options, convertible securities)

1. Title of Derivative Security (Instr. 4)	2. Date Exercisable and Expiration Date (Month/Day/Year)		3. Title and Amount of Securities Underlying Derivative Security (Instr. 4)		4. Conversion or Exercise Price of Derivative Security	5. Ownership Form: Direct (D) or Indirect (I) (Instr. 5)	6. Nature of Indirect Beneficial Ownership (Instr. 5)
	Date Exercisable	Expiration Date	Title	Amount or Number of Shares			
Performance Stock Units	(1)	(1)	Common Stock	575	(2)	D	
Performance Stock Units	(3)	(3)	Common Stock	757	(2)	D	
Performance Stock Units	(4)	(4)	Common Stock	1,002	(2)	D	
Performance Stock Units	(5)	(5)	Common Stock	923	(2)	D	
Performance Stock Units	(6)	(6)	Common Stock	884	(2)	D	
Performance Stock Units	(7)	(7)	Common Stock	827	(2)	D	
Performance Stock Units	(8)	(8)	Common Stock	933	(2)	D	
Performance Stock Units	(9)	(9)	Common Stock	845	(2)	D	
Performance Stock Units	(10)	(10)	Common Stock	815	(2)	D	
Performance Stock Units	(11)	(11)	Common Stock	799	(2)	D	

Explanation of Responses:

1. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending March 31, 2025. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 40.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on May 15, 2026 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
2. Each performance stock unit represents a contingent right to receive one share of Microchip Technology Incorporated common stock.
3. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending June 30, 2025. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 40.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on August 15, 2026 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
4. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending September 30, 2025. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 40.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on November 16, 2026 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
5. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending December 31, 2025. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 40.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on February 15, 2027 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
6. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending March 31, 2026. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 42.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on May 17, 2027 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
7. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending June 30, 2026. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 42.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on August 15, 2027 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
8. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending September 30, 2026. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 42.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on November 15, 2027 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
9. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending December 31, 2026. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 42.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on February 15, 2028 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

10. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending March 31, 2027. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 42.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on May 15, 2028 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

11. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending June 30, 2027. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 40.0% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest on August 15, 2028 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

Remarks:

Deborah L. Wussler, as Attorney-
in-Fact 08/30/2024

** Signature of Reporting Person Date

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

* If the form is filed by more than one reporting person, see Instruction 5 (b)(v).

** Intentional misstatements or omissions of facts constitute Federal Criminal Violations See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, see Instruction 6 for procedure.

Persons who respond to the collection of information contained in this form are not required to respond unless the form displays a currently valid OMB Number.

POWER OF ATTORNEY

Know all by these presents, that the undersigned hereby constitutes and appoints each of Ganesh Moorthy, Eric Bjornholt, and Deborah L. Wussler, signing singly, the undersigned's true and lawful attorney-in-fact to:

- (1) execute for and on behalf of the undersigned, in the undersigned's capacity as an officer and/or director of Microchip Technology Incorporated (the "Company"), Forms 3, 4, and 5 in accordance with Section 16(a) of the Securities Exchange Act of 1934 and the rules thereunder;
- (2) do and perform any and all acts for and on behalf of the undersigned which may be necessary or desirable to complete and execute any such Form 3, 4, or 5, complete and execute any amendment or amendments thereto, and timely file such form with the United States Securities and Exchange Commission and any stock exchange or similar authority; and
- (3) take any other action of any type whatsoever in connection with the foregoing which, in the opinion of such attorney-in-fact, may be of benefit to, in the best interest of, or legally required by, the undersigned, it being understood that the documents executed by such attorney-in-fact on behalf of the undersigned pursuant to this Power of Attorney shall be in such form and shall contain such terms and conditions as such attorney-in-fact may approve in such attorney-in-fact's discretion.

The undersigned hereby grants to each such attorney-in-fact full power and authority to do and perform any and every act and thing whatsoever requisite, necessary, or proper to be done in the exercise of any of the rights and powers herein granted, as fully to all intents and purposes as the undersigned might or could do if personally present, with full power of substitution or revocation, hereby ratifying and confirming all that such attorney-in-fact, or such attorney-in-fact's substitute or substitutes, shall lawfully do or cause to be done by virtue of this power of attorney and the rights and powers herein granted. The undersigned acknowledges that the foregoing attorneys-in-fact, in serving in such capacity at the request of the undersigned, are not assuming, nor is the Company assuming, any of the undersigned's responsibilities to comply with Section 16 of the Securities Exchange Act of 1934.

This Power of Attorney shall remain in full force and effect until the undersigned is no longer required to file Forms 3, 4, and 5 with respect to the undersigned's holdings of and transactions in securities issued by the Company, unless earlier revoked by the undersigned in a signed writing delivered to the foregoing attorneys-in-fact.

IN WITNESS WHEREOF, the undersigned has caused this Power of Attorney to be executed as of this 26th day of August, 2024.



Signature

Joseph Robert Krawczyk II
Printed Name

State of Arizona)
County of Maricopa) ss.

I, Carri L. Thompson, a Notary Public, do hereby certify that on this 26th day of August, 2024, personally appeared before me Joseph Robert Krawczyk II known to me to be the person whose name is subscribed to the foregoing instrument, and swore and acknowledged to me that he executed the same for the purposes and in the capacity therein expressed, and that the statements contained therein are true and correct.

Carri L. Thompson
Notary Public, State of Arizona

Name, Printed Carri L. Thompson
My Commission Expires Feb. 8, 2026



