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STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP

Check this box if no longer subject to Section 16. Form 4 or Form 5 obligations may continue. See Instruction 1(b).

Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934 or Section 30(h) of the Investment Company Act of 1940

Check this box to indicate that a transaction was made pursuant to a contract, instruction or written plan for the purchase or sale of equity securities of the issuer that is intended to satisfy the affirmative defense conditions of Rule 10b5-1(c). See Instruction 10.

1. Name and Address of Reporting Person <u>Bunker Mathew B</u> (Last) (First) (Middle) C/O MICROCHIP TECHNOLOGY INCORPORATED 2355 W CHANDLER BLVD (Street) CHANDLER AZ 85224-6199 (City) (State) (Zip)	2. Issuer Name and Ticker or Trading Symbol <u>MICROCHIP TECHNOLOGY INC [MCHP]</u>	5. Relationship of Reporting Person(s) to Issuer (Check all applicable) Director 10% Owner <input checked="" type="checkbox"/> Officer (give title below) Other (specify below) SENIOR VP, OPERATIONS
	3. Date of Earliest Transaction (Month/Day/Year) 04/01/2026	
4. If Amendment, Date of Original Filed (Month/Day/Year)		

Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned

1. Title of Security (Instr. 3)	2. Transaction Date (Month/Day/Year)	2A. Deemed Execution Date, if any (Month/Day/Year)	3. Transaction Code (Instr. 8)		4. Securities Acquired (A) or Disposed Of (D) (Instr. 3, 4 and 5)			5. Amount of Securities Beneficially Owned Following Reported Transaction(s) (Instr. 3 and 4)	6. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	7. Nature of Indirect Beneficial Ownership (Instr. 4)
			Code	V	Amount	(A) or (D)	Price			
Common Stock							25,158	D		

Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned (e.g., puts, calls, warrants, options, convertible securities)

1. Title of Derivative Security (Instr. 3)	2. Conversion or Exercise Price of Derivative Security	3. Transaction Date (Month/Day/Year)	3A. Deemed Execution Date, if any (Month/Day/Year)	4. Transaction Code (Instr. 8)		5. Number of Derivative Securities Acquired (A) or Disposed of (D) (Instr. 3, 4 and 5)		6. Date Exercisable and Expiration Date (Month/Day/Year)		7. Title and Amount of Securities Underlying Derivative Security (Instr. 3 and 4)	8. Price of Derivative Security (Instr. 5)	9. Number of derivative Securities Beneficially Owned Following Reported Transaction(s) (Instr. 4)	10. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	11. Nature of Indirect Beneficial Ownership (Instr. 4)
				Code	V	(A)	(D)	Date Exercisable	Expiration Date					
Restricted Stock Units	(1)	04/01/2026		A		5,529		(2)	(2)	Common Stock	\$0	5,529	D	
Performance Stock Units	(3)	04/01/2026		A		5,530		(4)	(4)	Common Stock	\$0	5,530	D	
Restricted Stock Units	(1)	04/01/2026		A		3,954		(5)	(5)	Common Stock	\$0	3,954	D	
Performance Stock Units	(3)	04/01/2026		A		3,954		(6)	(6)	Common Stock	\$0	3,954	D	
Restricted Stock Units	(1)	04/01/2026		A		2,294		(7)	(7)	Common Stock	\$0	2,294	D	
Performance Stock Units	(3)	04/01/2026		A		2,295		(8)	(8)	Common Stock	\$0	2,295	D	

Explanation of Responses:

- Each restricted stock unit represents a contingent right to receive one share of Microchip Technology Incorporated common stock.
- The restricted stock units vest in four equal quarterly installments on May 15, 2027, August 15, 2027, November 15, 2027 and February 15, 2028, as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
- Each performance stock unit represents a contingent right to receive one share of Microchip Technology Incorporated common stock.
- Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 4 quarters ending March 31, 2027. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 30.5% over the 4 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest 25% each on May 15, 2027, August 15, 2027, November 15, 2027 and February 15, 2028 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
- The restricted stock units vest in four equal quarterly installments on May 15, 2028, August 15, 2028, November 15, 2028 and February 15, 2029, as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
- Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 8 quarters ending March 31, 2028. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 31.0% over the 8 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest 25% each on May 15, 2028, August 15, 2028, November 15, 2028 and February 15, 2029 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.
- The restricted stock units vest in full on May 15, 2030, as long as the individual remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

8. Each Performance Stock Unit (PSU) granted under the Microchip Technology Incorporated (Microchip) 2004 Equity Incentive Plan represents a contingent right to receive shares of Microchip common stock based on Microchip's cumulative non-GAAP operating margin over a period of 12 quarters ending March 31, 2029. The target number of PSU shares that may be earned is reported in the table above and is based on Microchip achieving a cumulative non-GAAP operating margin of 31.5% over the 12 quarter measurement period. The actual number of shares that may be earned can be higher or lower than the target depending on Microchip's non-GAAP operating margin over the measurement period. Earned PSUs will vest in full on May 15, 2030 as long as the reporting person remains a service provider through the vesting date. Vested shares will be delivered to the reporting person upon vest.

Remarks:

Deborah L. Wussler, as Attorney-
in-Fact

04/03/2026

** Signature of Reporting Person

Date

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

* If the form is filed by more than one reporting person, see Instruction 4 (b)(v).

** Intentional misstatements or omissions of facts constitute Federal Criminal Violations See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, see Instruction 6 for procedure.

Persons who respond to the collection of information contained in this form are not required to respond unless the form displays a currently valid OMB Number.